

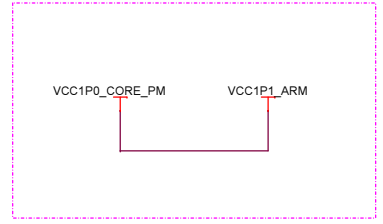
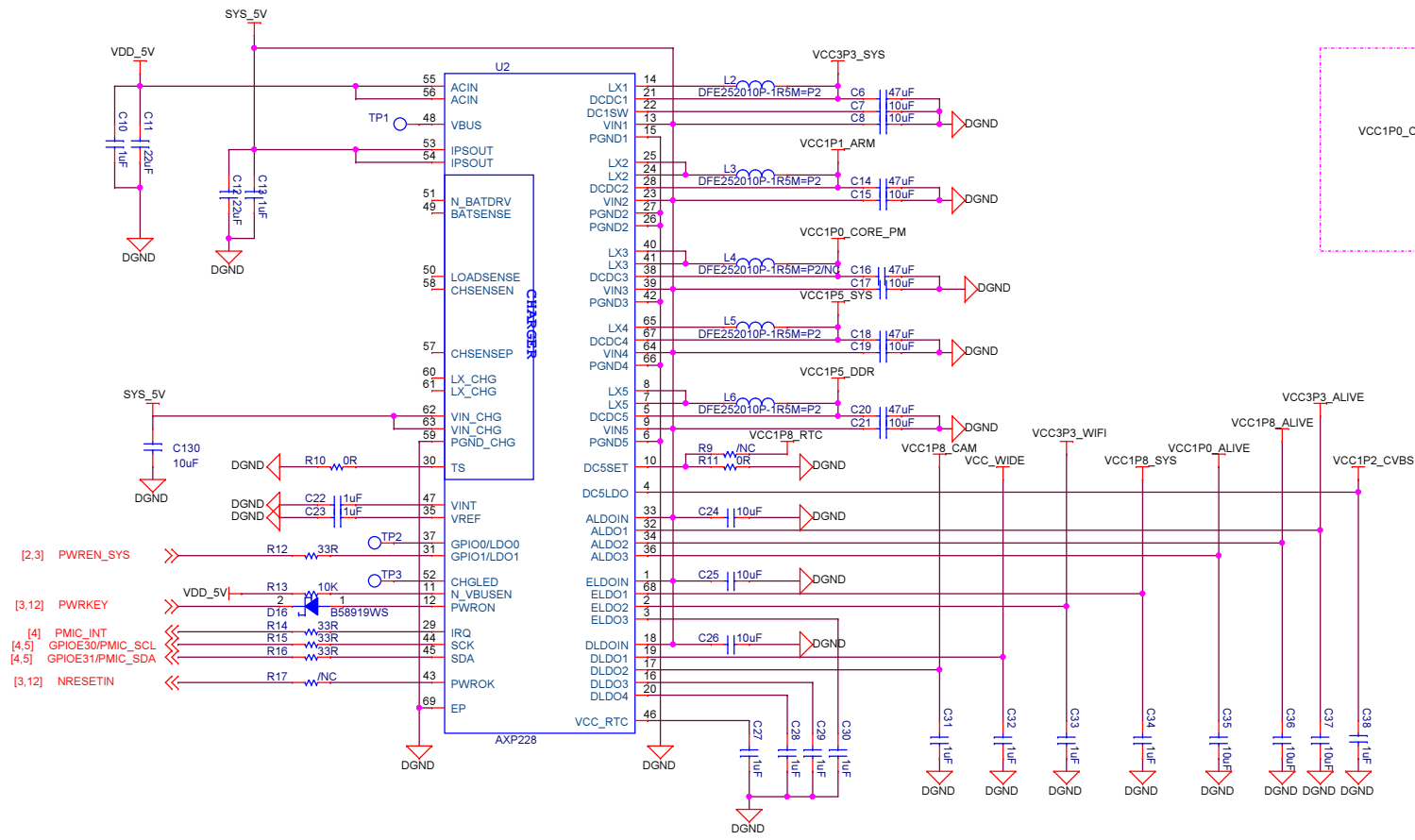
Smart4418

Revision

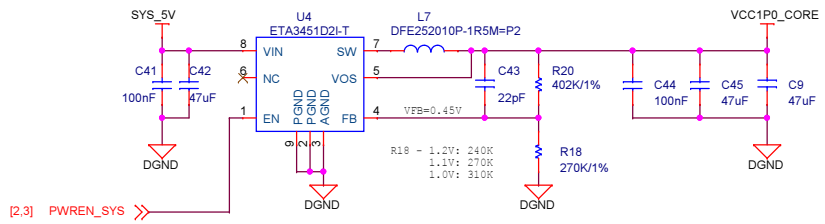
1512 , First Version



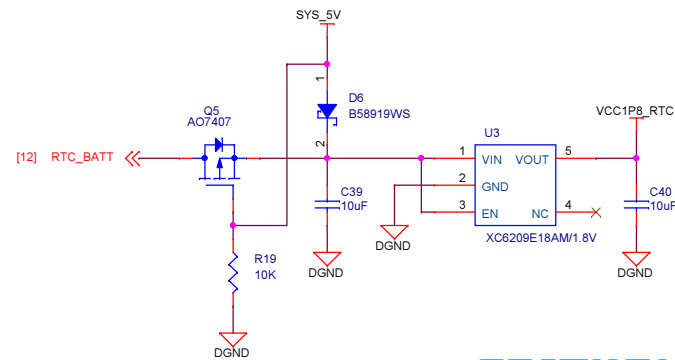
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VCC1P0_CORE_DC:1.1V

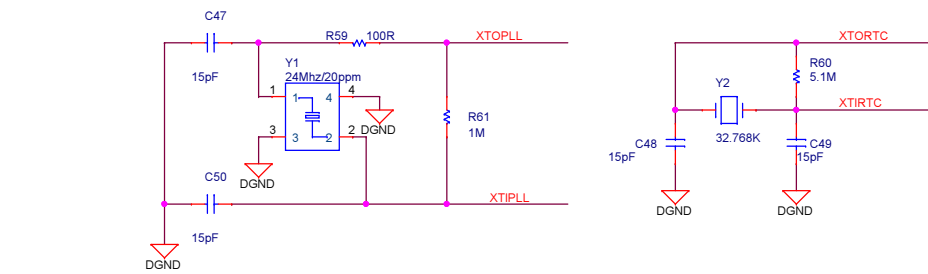
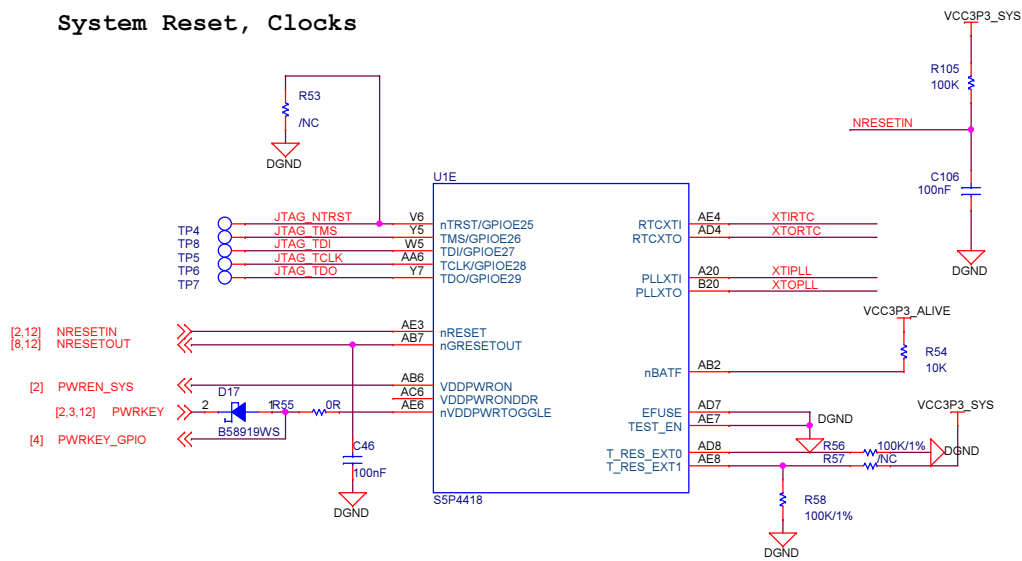


RTC Power

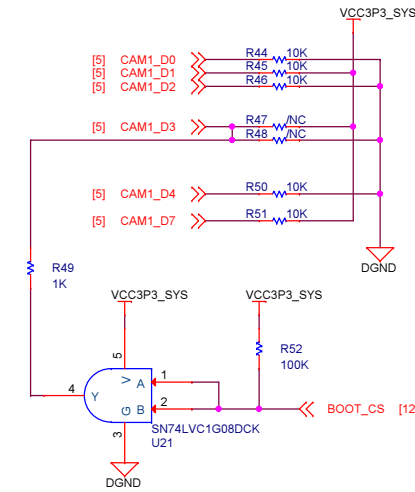
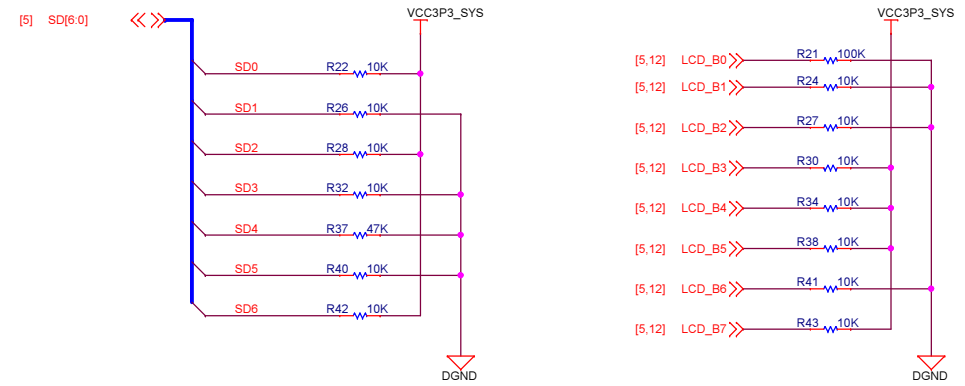


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System Reset, Clocks



Boot Mode Config



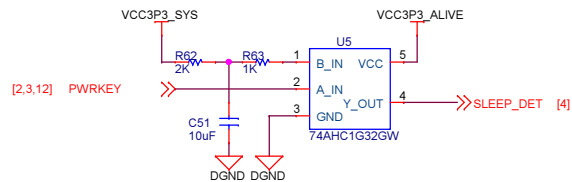
BOOT MODE OPTION

	eMMC	SPI	USB	NAND
SD0	HIGH	LOW	LOW	HIGH
SD1	LOW	LOW	HIGH	HIGH
SD2	HIGH	HIGH	HIGH	HIGH
SD4	LOW	HIGH		
SD5	LOW	LOW		

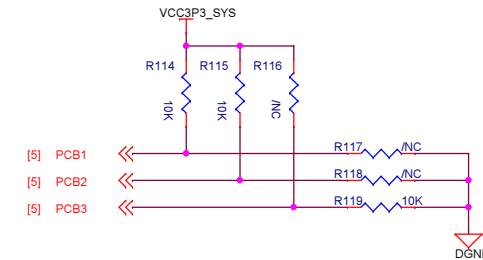
Boot media port select (SPI, eMMC)

	CH0	CH1	CH2
SD3	LOW	HIGH	LOW
CAM1_D3	LOW	LOW	HIGH

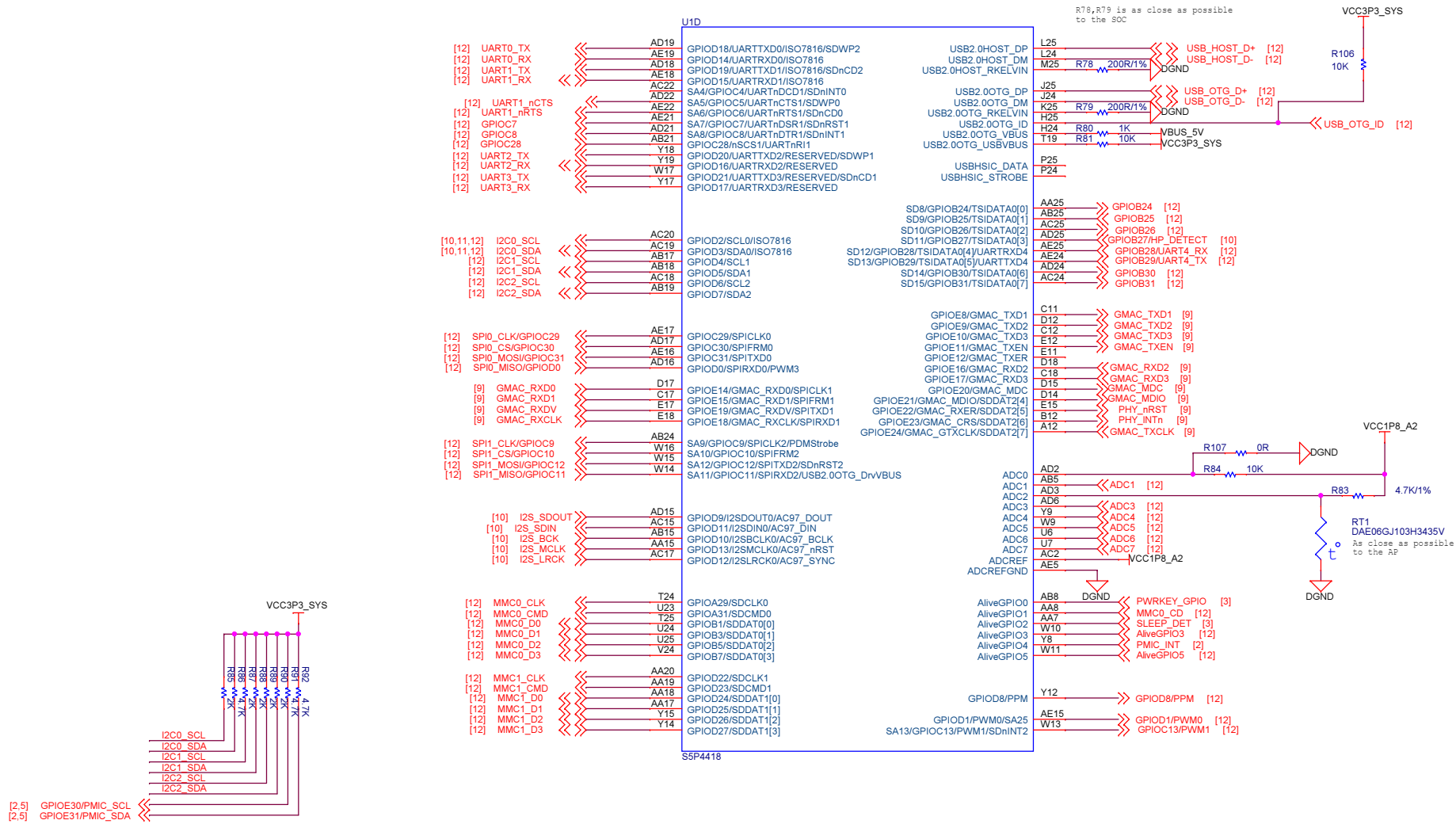
Sleep Detect



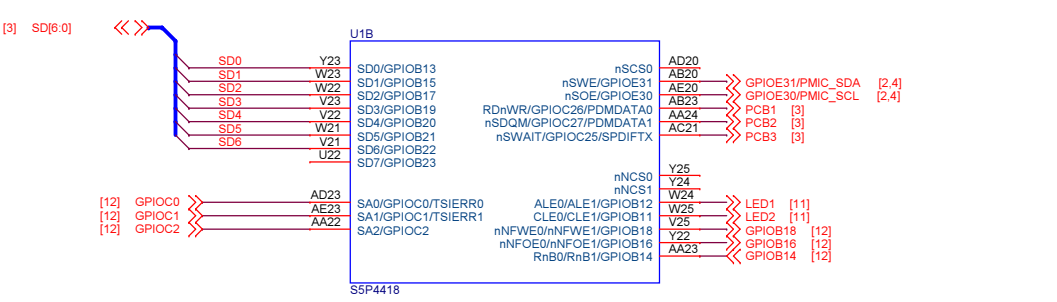
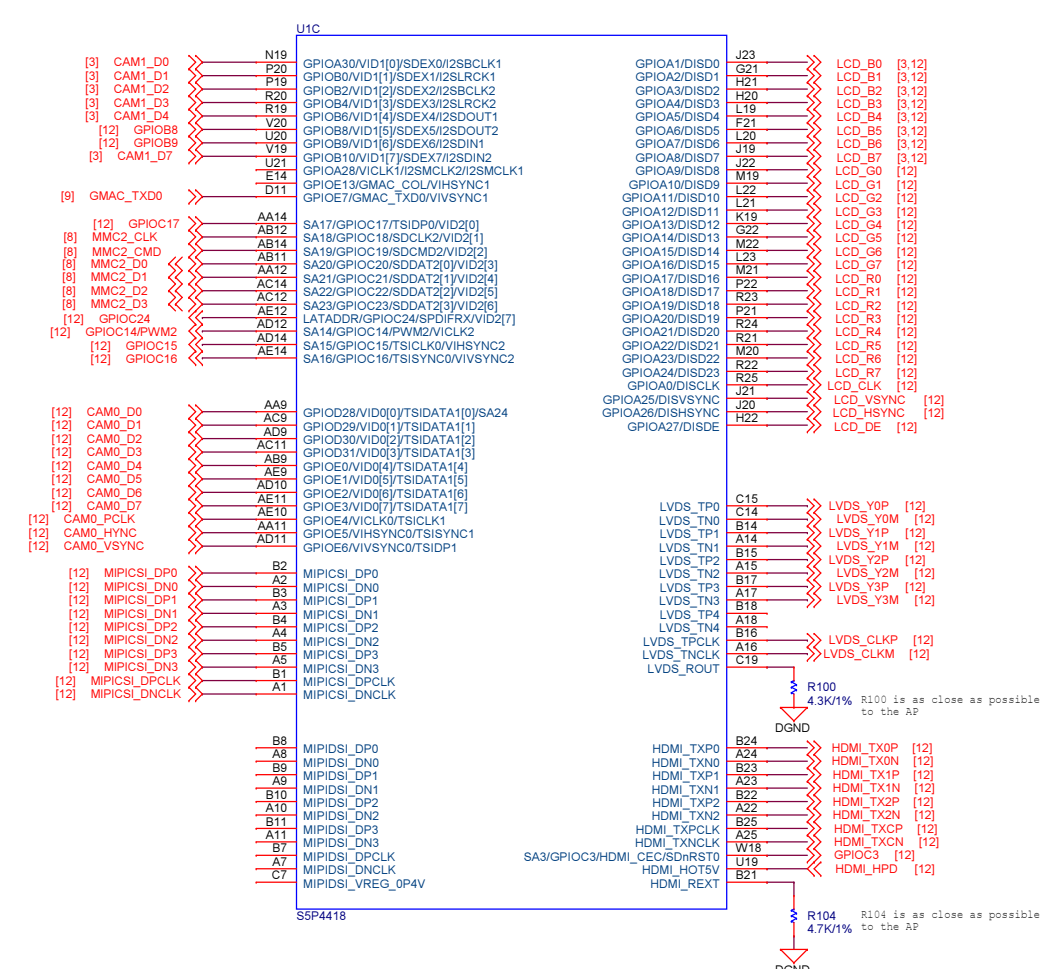
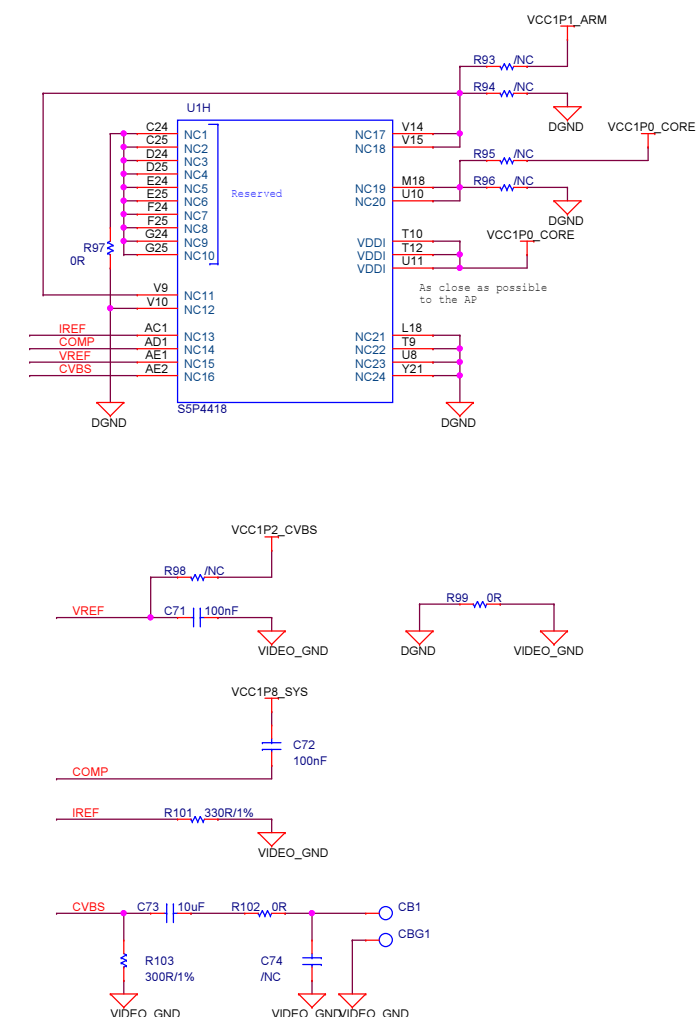
PCB Version



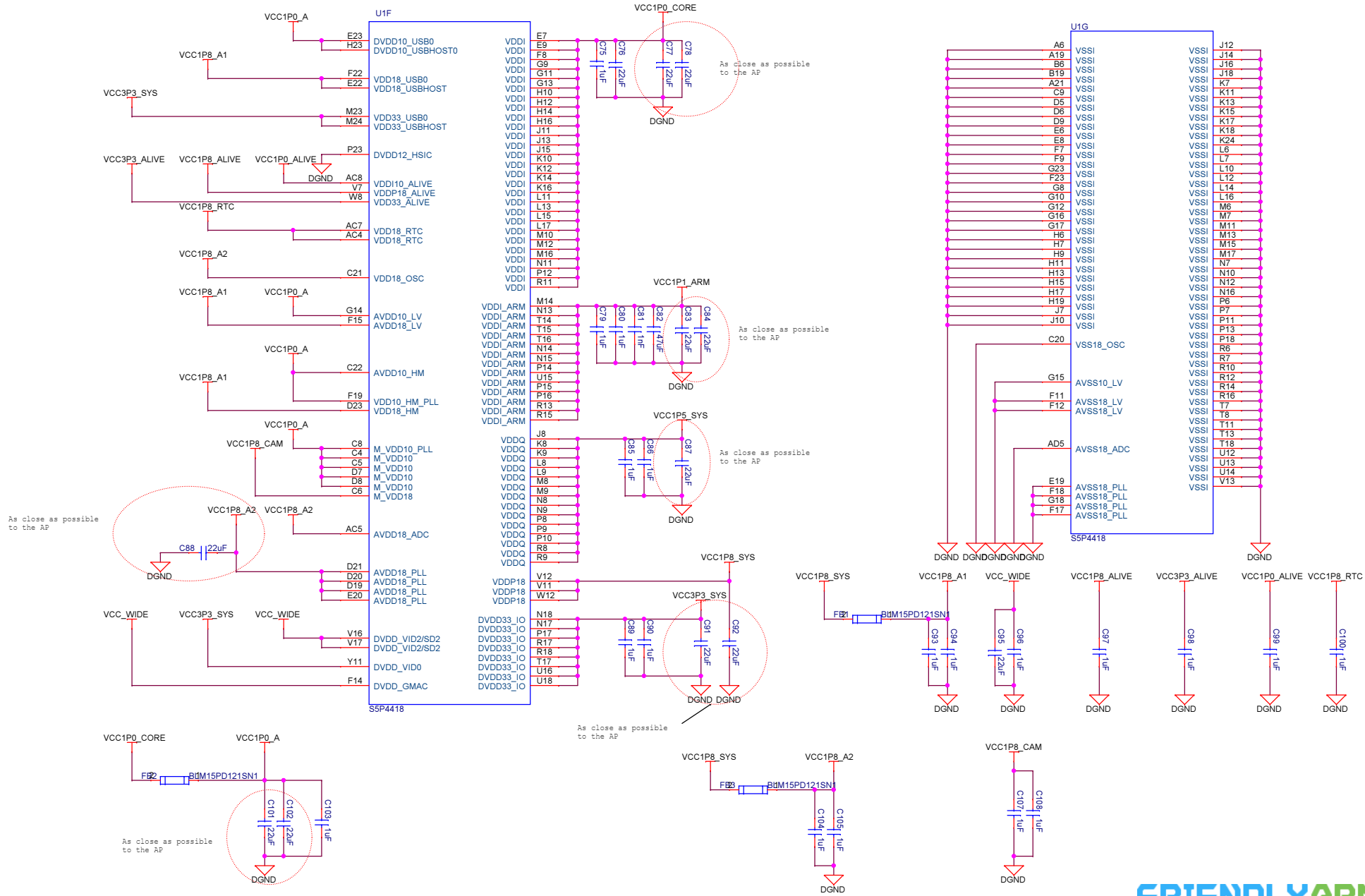
FRIENDLYARM



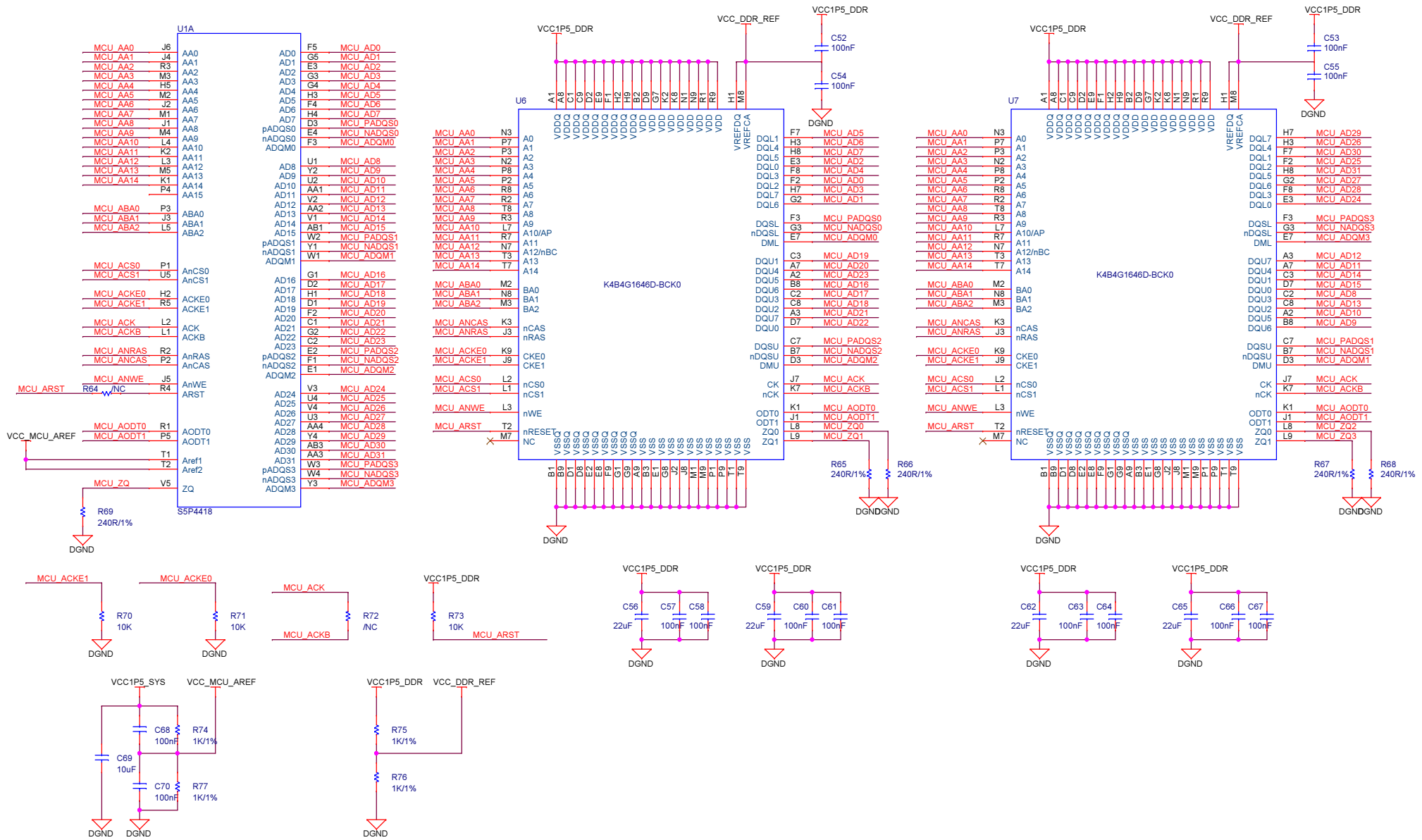
Reserved Function



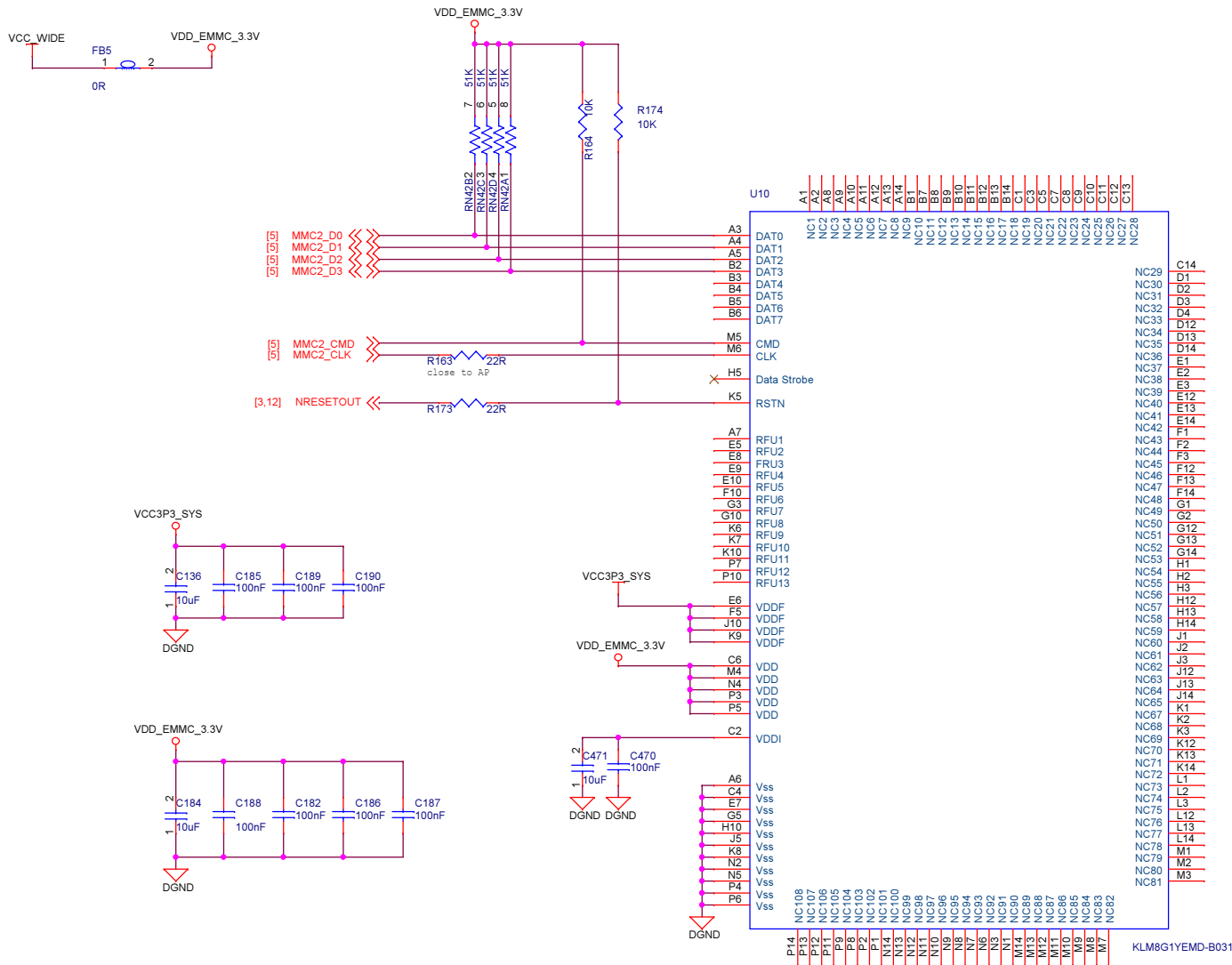
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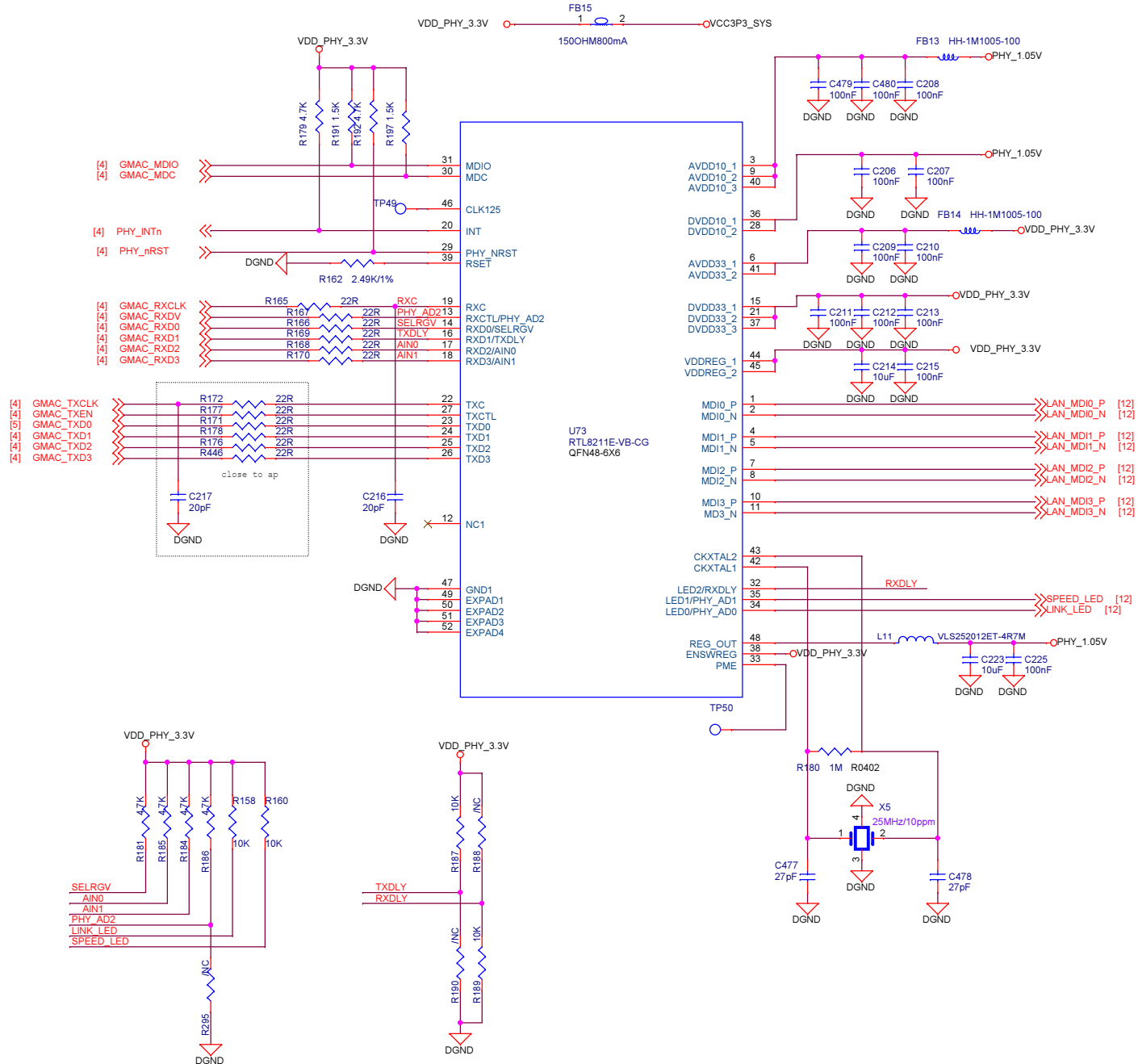
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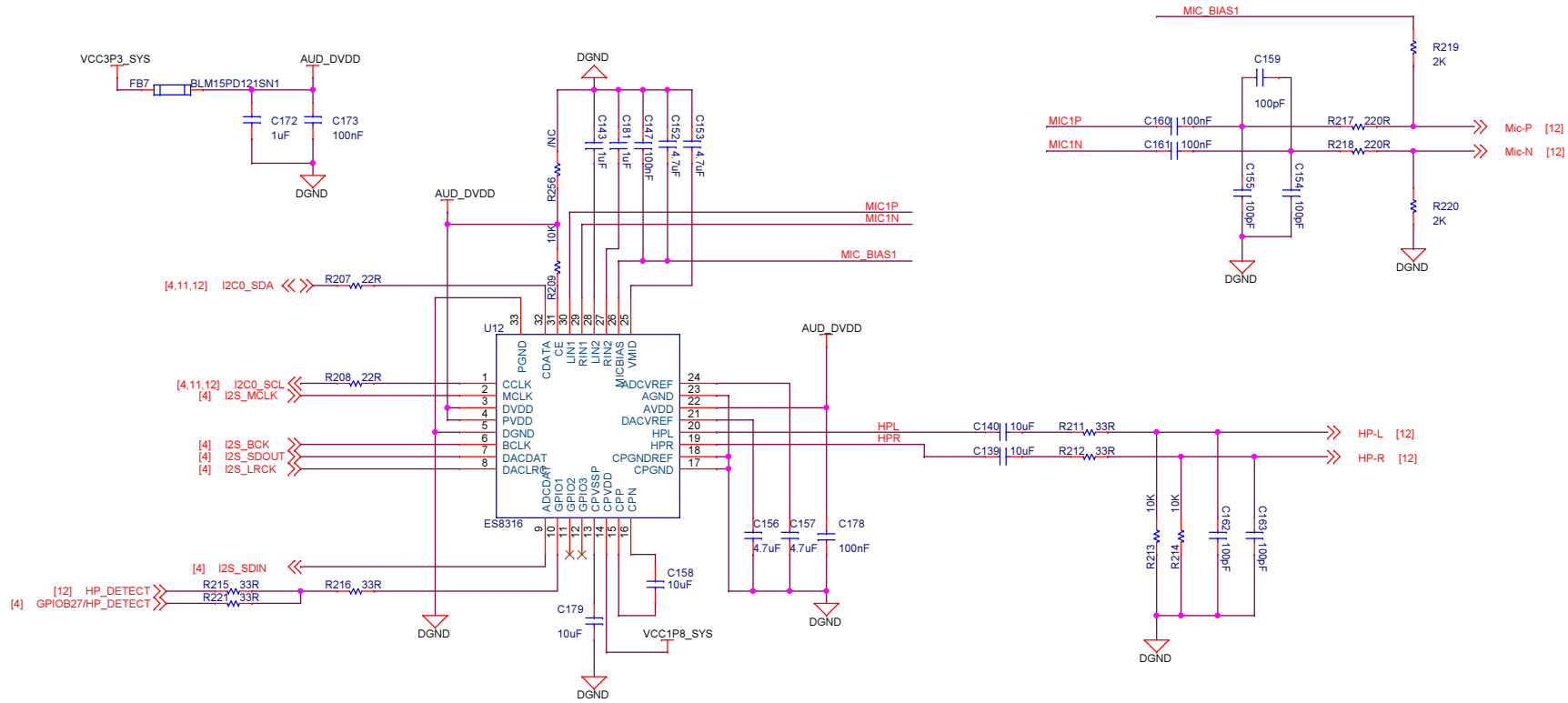


10/100/1000M Ethernet



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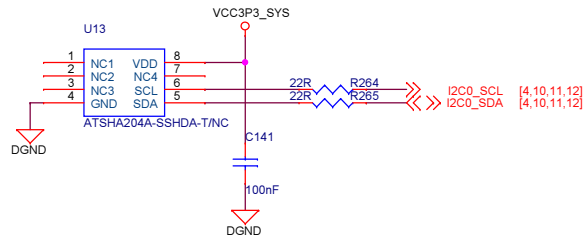
Audio



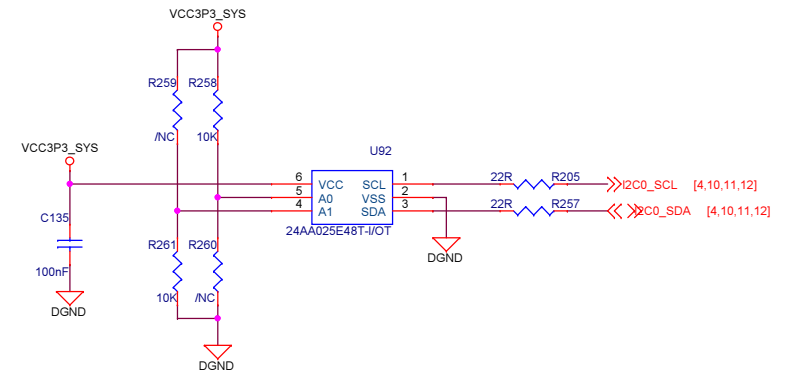
FRIENDLYARM

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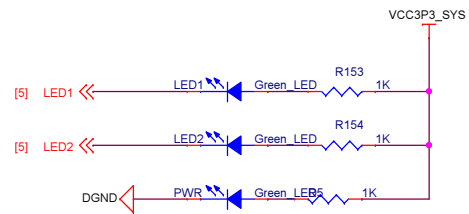
CryptoAuthentication



EEPROM with MAC Address

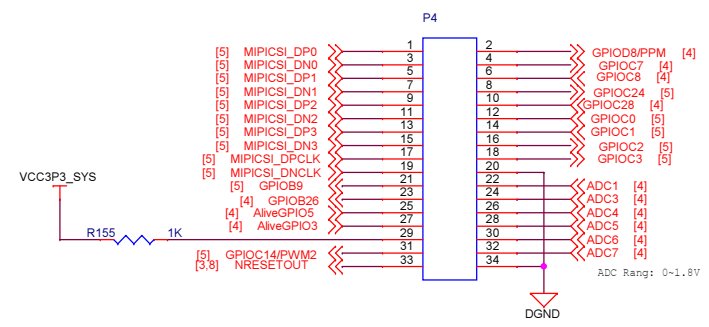
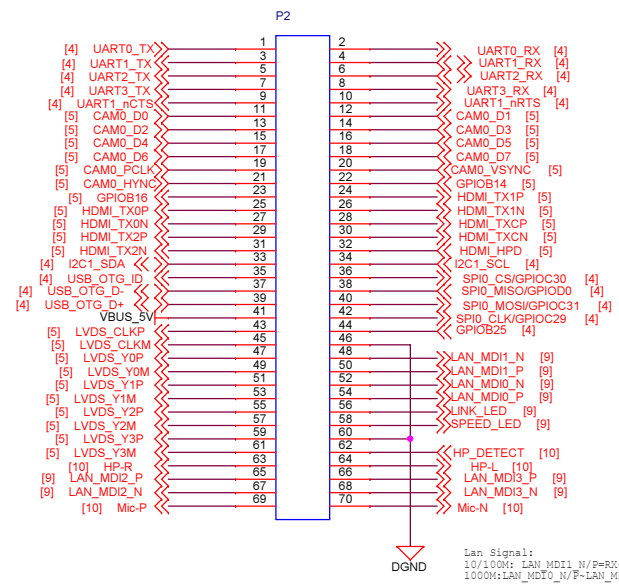
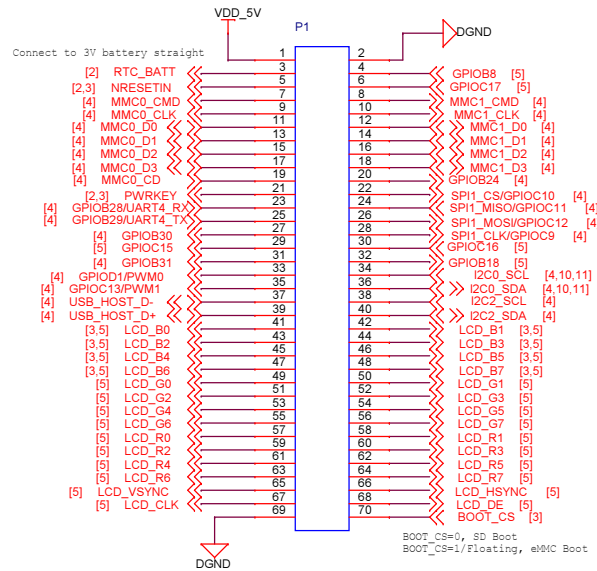


LEDs



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